

arm

TF-M configuration

September 2022

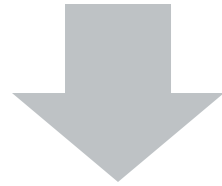
Config structure

Modules

1. Core
2. SPM
3. Platform
4. Partition_Crypto
5. Partition_ITS
6. Partition_PS
7. Partition_Attest
8. Partition_Audit
9. Partition_FWU
10. Partition_Platform
11. BL1
12. BL2
13. MCUBoot
14. Test
15. CC312
16. ADAC

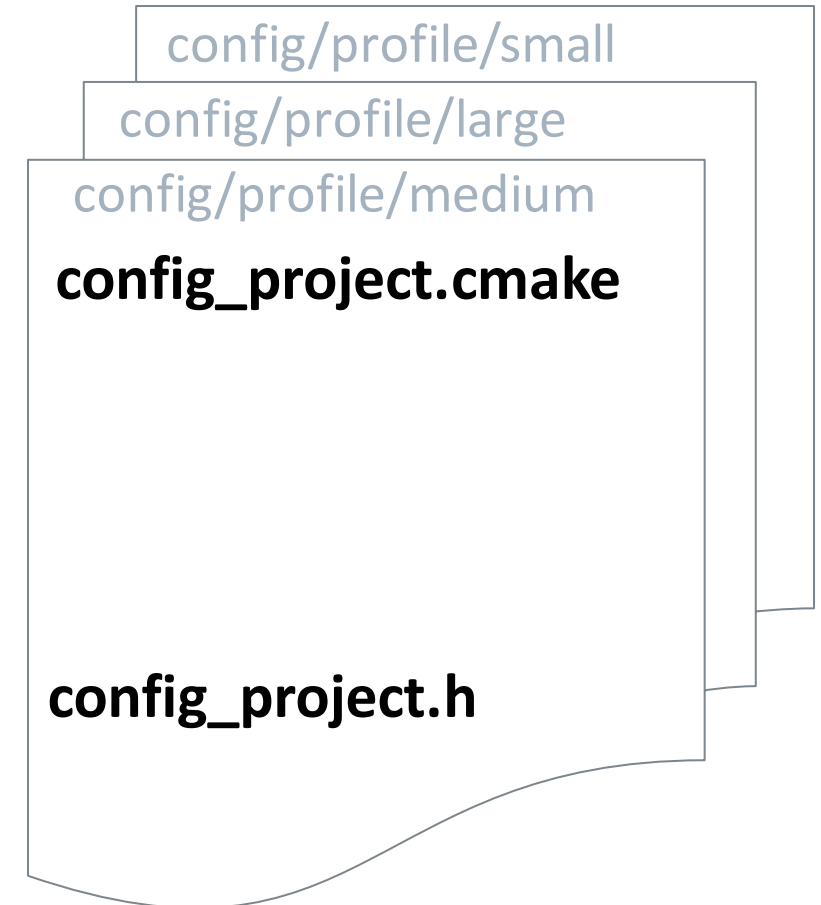
Build configs

- Defines file set for building
- Are CMake variables
- Cross dependent with other CMake variables
- Consistency checks (or KConfig ?)



Component options

- Feature selection in a source
- #define XXX
- Consistency check on option level



Config options

+ Core	+ TFM_SP_LOG_RAW_ENABLED	+ CONFIG_TFM_ENABLE_MVE	+ ITS_MAX_ASSET_SIZE	+ TFM_FWU_BOOTLOADER_LIB
+ TFM_PLATFORM	+ TFM_SPM_LOG_RAW_ENABLED	+ CONFIG_TFM_ENABLE_MVE_FP	+ ITS_NUM_ASSETS	+ PSA_FWU_MAX_BLOCK_SIZE
+ TFM_LIB_MODEL	Platform	+ Partition_Crypto	+ ITS_BUF_SIZE	+ TFM_FWU_BUF_SIZE
+ PSA_FRAMEWORK_HAS_MM_IOVEC	+ TFM_CODE_SHARING_PLATFORM_LISTS	+ CRYPTO_HW_ACCELERATOR	+ ITS_STACK_SIZE	+ FWU_STACK_SIZE
+ TFM_FIH_PROFILE	+ TFM_PXN_ENABLE	+ TFM_PARTITION_CRYPTO	Partition_PS	Partition_Platform
+ TFM_PARTITION_LOG_LEVEL	+ TFM_EXCEPTION_INFO_DUMP	+ CRYPTO_TFM_BUILTIN_KEYS_DRIVER	+ TFM_PARTITION_PROTECTED_STORAGE	+ TFM_PARTITION_PLATFORM
+ TFM_CODE_SHARING	+ CONFIG_TFM_FP	+ CRYPTO_ENGINE_BUF_SIZE	+ PS_CREATE_FLASH_LAYOUT	+ PLATFORM_SERVICE_INPUT_BUFFER_SIZE
+ NUM_MAILBOX_QUEUE_SLOT	+ TFM_PLAT_SPECIFIC_MULTI_CORE_COMM	+ CRYPTO_CONC_OPER_NUM	+ PS_ENCRYPTION	+ PLATFORM_SERVICE_OUTPUT_BUFFER_SIZE
+ TFM_DUMMY_PROVISIONING	+ DEBUG_AUTHENTICATION	+ CRYPTO_RNG_MODULE_DISABLED	+ PS_RAM_FS	
+ FORWARD_PROT_MSG	+ SECURE_UART1	+ CRYPTO_KEY_MODULE_DISABLED	+ PS_ROLLBACK_PROTECTION	
+ TFM_PSA_API	+ OTP_NV_COUNTERS_RAM_EMULATION	+ CRYPTO_AEAD_MODULE_DISABLED	+ PS_VALIDATE_METADATA_FROM_FLASH	
+ PSA_FRAMEWORK_ISOLATION_LEVEL	+ TFM_NS_NV_COUNTER_AMOUNT	+ CRYPTO_MAC_MODULE_DISABLED	+ PS_MAX_ASSET_SIZE	
+ CONFIG_TFM_FLOAT_ABI	+ PLATFORM_DEFAULT_BL1	+ CRYPTO_HASH_MODULE_DISABLED	+ PS_NUM_ASSETS	
+ CONFIG_TFM_ENABLE_CP10CP11	+ PLATFORM_DEFAULT_ATTEST_HAL	+ CRYPTO_CIPHER_MODULE_DISABLED	+ PS_CRYPTO_AEAD_ALG	
SPM	+ PLATFORM_DEFAULT_NV_COUNTERS	+ CRYPTO_ASYM_SIGN_MODULE_DISABLED	+ PS_STACK_SIZE	
+ TFM_ISOLATION_LEVEL	+ PLATFORM_DEFAULT_CRYPTO_KEYS	+ CRYPTO_ASYM_ENCRYPT_MODULE_DISABLED	Partition_Attest	
+ CONFIG_TFM_CONN_HANDLE_MAX_NUM	+ PLATFORM_DEFAULT_ROTPK	+ CRYPTO_KEY_DERIVATION_MODULE_DISABLED	+ TFM_PARTITION_INITIAL_ATTESTATION	
+ CONFIG_TFM_SPM_BACKEND	+ PLATFORM_DEFAULT_IAK	+ CRYPTO_IOVEC_BUFFER_SIZE	+ SYMMETRIC_INITIAL_ATTESTATION	
+ TFM_SPM_LOG_LEVEL	+ PLATFORM_DEFAULT_UART_STDOUT	+ CRYPTO_NV_SEED	+ ATTEST_INCLUDE_OPTIONAL_CLAIMS	
+ CONFIG_TFM_HALT_ON_CORE_PANIC	+ PLATFORM_DEFAULT_NV_SEED	+ CRYPTO_SINGLE_PART_FUNCS_DISABLED	+ ATTEST_INCLUDE_COSE_KEY_ID	
+ CONFIG_TFM_LAZY_STACKING	+ PLATFORM_DEFAULT_OTP	+ CRYPTO_STACK_SIZE	+ ATTEST_TOKEN_PROFILE	
+ CONFIG_TFM_ENABLE_FPU	+ PLATFORM_DEFAULT_OTP_WRITEABLE	Partition_ITS	+ ATTEST_STACK_SIZE	
+ CONFIG_TFM_DOORBELL_API	+ PLATFORM_DEFAULT_PROVISIONING	+ TFM_PARTITION_INTERNAL_TRUSTED_STORAGE	+ ATTEST_INCLUDE_TEST_CODE	
+ CONFIG_TFM_STACK_WATERMARKS	+ PLATFORM_JS_FVP	+ ITS_CREATE_FLASH_LAYOUT	Partition_Audit	
+ CONFIG_TFM_SPM_BACKEND_IPC	+ ARM_V80M_ARCH	+ ITS_RAM_FS	+ TFM_PARTITION_AUDIT_LOG	
+ CONFIG_TFM_SPM_BACKEND_SFN	+ PLATFORM_HAS_ISOLATION_L3_SUPPORT	+ ITS_VALIDATE_METADATA_FROM_FLASH	Partition_FWU	
+ TFM_PARTITION_NS_AGENT_MAILBOX	+ PLATFORM_SP_STACK_SIZE		+ TFM_PARTITION_FIRMWARE_UPDATE	
+ TFM_PARTITION_NS_AGENT_TZ	+ CONFIG_TFM_ENABLE_FP			

User level → Kconfig

Advanced → config_XXX.h