Linaro Connect LVC21F

Session Resources List

AI/ML

LVC21F-212 Development of Deep Learning Library for AArch64 CPU — https://connect.linaro.org/resources/lvc21f/lvc21f-212

LVC21F-213 Developing to deploying TinyML models in minutes! — https://connect.linaro.org/resources/lvc21f/lvc21f-213

LVC21F-318 Testing TensorFlow AARCH64 packages — https://connect.linaro.org/resources/lvc21f/lvc21f-318

LVC21F-319 TVM for micro targets — https://connect.linaro.org/resources/lvc21f/lvc21f-319

Android

LVC21F-308 Universal opensource gralloc for Android. Concepts from the GloDroid team. — https://connect.linaro.org/resources/lvc21f/lvc21f-308

LVC21F-307 Leveraging the past for the future of AOSP devboards — https://connect.linaro.org/resources/lvc21f/lvc21f-307

LVC21F-306 AOSP Currency — https://connect.linaro.org/resources/lvc21f/lvc21f-306

Arm Architecture

LVC21F-113 Arm Architecture 2021 Extensions — https://connect.linaro.org/resources/lvc21f/lvc21f-113

Automotive

LVC21F-100K1 Keynote: From Mobile to Automotive: Delivering Intelligent, Next-Gen Digital Cockpit Solutions — https://connect.linaro.org/resources/lvc21f/lvc21f-100k1

LVC21F-300K1 Keynote: Innovation and disruption of software and hardware architecture in automotive — https://connect.linaro.org/resources/lvc21f/lvc21f-300k1

Big Data

LVC21F-201 Apache Arrow: A unified language across Big Data — https://connect.linaro.org/resources/lvc21f/lvc21f-201

LVC21F-202 BIGTOP 3.0 with the upgraded Mpack: New era of BigData Distribution — https://connect.linaro.org/resources/lvc21f/lvc21f-202

Boot Architecture

LVC21F-315 YAML as a new Device Tree source format — https://connect.linaro.org/resources/lvc21f/lvc21f-315

CodeLinaro

LVC21F-100K2 Keynote: The CodeLinaro Story — https://connect.linaro.org/resources/lvc21f/lvc21f-100k2

LVC21F-101 CodeLinaro Integrated — https://connect.linaro.org/resources/lvc21f/lvc21f-101

Data Center

LVC21-100K Keynote: Broadening the Hyperscalers Landscape — https://connect.linaro.org/resources/lvc21f/lvc21-100k

LVC21F-203 The Progress for Distributed Storage on Arm64 — https://connect.linaro.org/resources/lvc21f/lvc21f-203

LVC21F-204 Secure Container Runtime on ARM — https://connect.linaro.org/resources/lvc21f/lvc21f-204

LVC21F-205 Performance Optimization of ceph all-flash Based on aarch64 — https://connect.linaro.org/resources/lvc21f/lvc21f-205

LVC21F-206 Arm Datacenter Ecosystem Landscape - A Centralized Info Hub For All Arm Developers — https://connect.linaro.org/resources/lvc21f/lvc21f-206

LVC21F-207 Firmware first error handling on Arm Neoverse Reference design platforms. — https://connect.linaro.org/resources/lvc21f/lvc21f-207

LVC21F-219 Tips and Tricks to tune your software for ARM   https://connect.linaro.org/resources/lvc21f/lvc21f-219

LVC21F-220 Opensource Database Ecosystem on ARM   https://connect.linaro.org/resources/lvc21f/lvc21f-220

LVC21F-200K2 Keynote: Linaro's Next Decade: Scaling the ARM Ecosystem for 20x
Growth   https://connect.linaro.org/resources/lvc21f/lvc21f-200k2

IoT and Embedded

LVC21F-111 Fleet Monitoring Solutions for IoT   https://connect.linaro.org/resources/lvc21f/lvc21f-111

LVC21F-112 Picolibc: A C Library for Smaller Systems   https://connect.linaro.org/resources/lvc21f/lvc21f-112

LVC21F-208 Floating-point support in Trusted Firmware–M   https://connect.linaro.org/resources/lvc21f/lvc21f-208

LVC21F-209 Interrupt Handling in Trusted Firmware M   https://connect.linaro.org/resources/lvc21f/lvc21f-209

LVC21F-210 TF-M implementation update of FF-M 1.1 new features   https://connect.linaro.org/resources/lvc21f/lvc21f-210

LVC21F-211 Tiny ONNC: MLIR-based AI compiler for ARM IoT devices https://connect.linaro.org/resources/lvc21f/lvc21f-211

LVC21F-302 Is the Distro (Still) Relevant in Embedded Linux?   https://connect.linaro.org/resources/lvc21f/lvc21f-302

LVC21F-303 Secure Sensor Data Pipeline   https://connect.linaro.org/resources/lvc21f/lvc21f-303

LVC21F-304 ACPI overlays for embedded devices   https://connect.linaro.org/resources/lvc21f/lvc21f-304

LVC21F-305 LEDGE RP as a development environment for secure boot https://connect.linaro.org/resources/lvc21f/lvc21f-305

Linux Kernel

LVC21F-107 Unleashing the performance of multi-actuator drives   https://connect.linaro.org/resources/lvc21f/lvc21f-107

LVC21F-108 Advanced KMS: virtualized planes as a way to hide hardware
implementation details   https://connect.linaro.org/resources/lvc21f/lvc21f-108

LVC21F-109 Qualcomm upstream status update   https://connect.linaro.org/resources/lvc21f/lvc21f-109

LVC21F-110 Performance improvements on Qualcomm SoCs   https://connect.linaro.org/resources/lvc21f/lvc21f-110

LVC21F-222 Upstreaming the Qualcomm modems in Linux Kernel - SDX55 case
study   https://connect.linaro.org/resources/lvc21f/lvc21f-222

LVC21F-223 Demystifying Linux MIPI DSI Bridge Subsystem   https://connect.linaro.org/resources/lvc21f/lvc21f-223

LVC21F-316 KWG lightning talk   https://connect.linaro.org/resources/lvc21f/lvc21f-316

LVC21F-317 Rust for Linux   https://connect.linaro.org/resources/lvc21f/lvc21f-317

Multimedia

LVC21F-119 Master your PipeWire streams with WirePlumber   https://connect.linaro.org/resources/lvc21f/lvc21f-119

Open Source Development

LVC21F-105 CI/CD/CC: open source compliance integrated in development.   https://connect.linaro.org/resources/lvc21f/lvc21f-105

LVC21F-106 Sound Open Firmware meets Zephyr   https://connect.linaro.org/resources/lvc21f/lvc21f-106

Security

LVC21F-114 LVC21F-114 Enhance Linux Kernel protection with Trusted Execution
Environment   https://connect.linaro.org/resources/lvc21f/lvc21f-114

LVC21F-116 Assessing the effectiveness of MCUBoot protections against fault
injection attacks   https://connect.linaro.org/resources/lvc21f/lvc21f-116

LVC21F-117 Secure Partition Manager for Arm Cortex-A   https://connect.linaro.org/resources/lvc21f/lvc21f-117

LVC21F-118 Firmware Standards and Security   https://connect.linaro.org/resources/lvc21f/lvc21f-118

Standardized Firmware

LVC21F-310 The best practice for the SystemReady IR | https://connect.linaro.org/resources/lvc21f/lvc21f-310

LVC21F-311 Overview of Firmware Architecture for Arm CCA | https://connect.linaro.org/resources/lvc21f/lvc21f-311

LVC21F-312 TrustedFirmware.org panel discussion | https://connect.linaro.org/resources/lvc21f/lvc21f-312

LVC21F-313 A firmware journey through standards to Arm SystemReady certification | https://connect.linaro.org/resources/lvc21f/lvc21f-313

LVC21F-314 Standardizing the Arm servers side-band management interface | https://connect.linaro.org/resources/lvc21f/lvc21f-314

LVC21F-309 SystemReady Panel | https://connect.linaro.org/resources/lvc21f/lvc21f-309

Testing and CI

LVC21F-102 A 2021 update on LKFT | https://connect.linaro.org/resources/lvc21f/lvc21f-102

LVC21F-103 Building, booting, and testing Linux with 1 command (locally and at scale) | https://connect.linaro.org/resources/lvc21f/lvc21f-103

Tools

LVC21F-321 The LLVM Embedded Toolchain for Arm, a new open source toolchain | https://connect.linaro.org/resources/lvc21f/lvc21f-321

Virtualization

LVC21F-216 AArch64 UEFI & ACPI on Cloud Hypervisor: A step further to modern virtualization | https://connect.linaro.org/resources/lvc21f/lvc21f-216

LVC21F-217 Shared Virtual Addressing for high performance Arm Infrastructure platforms | https://connect.linaro.org/resources/lvc21f/lvc21f-217

LVC21F-218 De-mystifying GFX-virtualization with VirGL | https://connect.linaro.org/resources/lvc21f/lvc21f-218

LVC21F-224 Gunyah Open Source Hypervisor" a modern scalable virtualization framework | https://connect.linaro.org/resources/lvc21f/lvc21f-224

Windows on Arm

LVC21F-301 Python on Windows on Arm - Initial Progress Report and Discussion | https://connect.linaro.org/resources/lvc21f/lvc21f-301

LVC21F-300K2 Keynote: Windows on Arm panel | https://connect.linaro.org/resources/lvc21f/lvc21f-300k2

96Boards

LVC21F-120 Berets: Open-design, cross-platform robotics carrier boards | https://connect.linaro.org/resources/lvc21f/lvc21f-120

LVC21F-214 96Boards, Drones and PX4 Update | https://connect.linaro.org/resources/lvc21f/lvc21f-214

LVC21F-215 96Boards CEv2 Specification Intro | https://connect.linaro.org/resources/lvc21f/lvc21f-215