

## TF OpenCI Triage Meeting Minutes

(Sent to: tf-openci-triage@lists.trustedfirmware.org)

Mar 3, 2026

Minutes - Kanban board: <https://linaro.atlassian.net/jira/software/c/projects/TFC/boards/70>

- **TF- A** (Olivier/Joanna)
  - <https://linaro.atlassian.net/browse/TFC-828> The tf-a-lts-main job shows as failed when the job was skipped
    - High
    - Done
    - Arthur: The patch has been merged. I'll monitor the recent job runs to confirm that the issue has been fully resolved.
  - <https://linaro.atlassian.net/browse/TFC-834> Arm-On-Arm (Epic)
    - Medium
    - On Hold
    - Arthur: Synced with Chris, and created a ticket. This epic will be put on hold, until I complete Hafnium LTS automation, and two Chromebook OpenCI integration
- **TF-M** (Anton/Matt)
  - <https://linaro.atlassian.net/browse/TFC-784> Allow scripts to run alongside tests running for TF-M on LAVA
    - High
    - In Progress
    - Arthur: waiting for **\*Matt\*** to update the tf-m-ci-script to build TF-M for the FVP. after that I'll create a patch to generate LAVA test job - next two days
  - <https://linaro.atlassian.net/browse/TFC-788> Enable STM32WBA6 TF-M CI loop
    - High
    - **Blocked**
    - Arthur: patch submitted, However, there's a PSA crypto issue on all STM platforms.I've inform ST, waiting for their fix on the issue
- **Hafnium**
  - <https://linaro.atlassian.net/browse/TFC-779> Hafnium LTS: Automate generation of change log for minor releases based
    - Medium
    - In Progress
    - Arthur: Working on the new Jobs
- **MbedTLS**
  - n/a
- **OpenCI/Infrastructure**
  - <https://linaro.atlassian.net/browse/TFC-833> FVP usage in CI with armlm lookup

- Major
  - In progress
  - Arthur: handed to Ben, Ben has submitted a fix, then new images need building for tux - wed- thurs should be fine.
- <https://linaro.atlassian.net/browse/TFC-799> migration of tux FVP to Arm64
  - High
  - **Blocked**
  - Ben waiting for FVP images
  - Chris - FVP images need passing - mid mar
- **Board Changes**
  - **ST**
    - <https://linaro.atlassian.net/browse/TFC-786> Gradual phase out of STM32L5 and replacement with STM32WBA6
      - **Blocked** by TF-M TFC-788
  - **Google**
    - <https://linaro.atlassian.net/browse/TFC-653> Google Platform 2025
      - New Chrome books have arrived, in que
- **Afterparty Items**
  - **Tux limit hit on Friday (day of reset) - We'll look at the limit numbers again**
    - Staging jobs to be checked by Chris as usage is high
  - **Ben proposed using Shrinkwrap for private TF FVP images**
    - Hes created a ticket here <https://linaro.atlassian.net/browse/TFC-840>
  - **Arthur on holiday 2nd - 6th Mar**