

TF OpenCI Triage Meeting Minutes

(Sent to: tf-openci-triage@lists.trustedfirmware.org)

Aug 25, 2026

Kanban board: <https://linaro.atlassian.net/jira/software/c/projects/TFC/boards/70>

Mailing list: tf-openci-triage@lists.trustedfirmware.org - Join [here](#)

- **TF- A** (Olivier/Joanna)
 - Arthur Mentioned upcoming TF-A LTS work post meeting with maintainers.
 - <https://linaro.atlassian.net/browse/TFC-834> Arm-On-Arm (Epic)
 - Medium
 - **In Progress**
 - <https://linaro.atlassian.net/browse/TFC-799> migration of tux FVP to Arm64
 - High
 - **In Progress**
 - Ben waiting for FVP images
 - Wrote a PR for FVP docker repo which uses buildx - Chris reviewing and merging.. Needs a test and start uploading some arm64 images
 - **Noticed a few failed fvp dockers which have now been patched. We are now ready to do a Arm64 builds. Ben to make CI changes with Chris. Chris to do upload**
 - <https://linaro.atlassian.net/browse/TFC-850> TF-A LTS automation
 - <https://linaro.atlassian.net/browse/TFC-866> Automate LTS Patch Stack Creation from Triage Reports
 - Arthur: Provided feedback to Manish
- **Ben mentioned to Chris On the code coverage on the website its showing NA**
 - It uses the last successful but not if that is older than something like 50 builds. It looks for code coverage report. (complete pipeline) Last job passed is now **17th** so it has a **green tick**. Chris - Job retention is only 3 days so link is dead. **Need to decide how to track going forward**

- **TF-M** (Anton/Matt)
 - <https://linaro.atlassian.net/browse/TFC-784> Allow scripts to run alongside tests running for TF-M on LAVA
 - High
 - **Blocked**
 - Arthur: Blocked: Task on hold due to [broken DCSU builds](#) in TF-M
 - On hold nobody to fix - Matt to check - Long Term blocked - No changes not a huge priority RSE internal branch is ok public one broken.
- <https://linaro.atlassian.net/browse/TFC-863> Enable PSA API tests to run with Musca B1 in LAVA
 - Matt - Running PSA test on MUSCAB1 seems to have some timing problem- by the time the log is read its halfway through the test.
 - **Looks to have a solution but Ben is still working on migrating devices to LAA. Anton to review**
- **Hafnium**
 - n/a
- **MbedTLS**
 - n/a
- **OpenCI/Infrastructure**
 - <https://linaro.atlassian.net/browse/TFC-13> IAR Compiler Support for TF-M and MbedTLS
 - **On Hold**
 - Passed the license details and handed over the TF-M tasks, [TFC-618](#), [TFC-619](#), to Saheer and Matt. Matt taking a look. Matt to try locally Arm ticket status updated- **Some work on Matt to check licensing. - Anton sent a mail about licenses as we just need a token.**
 - TFC-857 N1SDP only one in the lab, old aging board
 - Olivier to take a look to see if one can be sourced
 - Found 1 or 2 good devices
 - Ben has found three within Linaro not being used. One might be a morello
 - Ben to talk to Dean @ Arm
 - **Ben: still waiting for devices from Dean/Saheer**
 - **Joanna : Mention Junos too**

- Dashboard,
 - See TF-A
- **Board Changes**
 - **Google**
 - <https://linaro.atlassian.net/browse/TFC-409> TF-A: Add Geralt to Google Platform in 2024
 - <https://linaro.atlassian.net/browse/TFC-684> TFC: Enable Geralt Chromebook on TF LAVA
 - Inprogress
 - Arthur: resolved the power off command issue
 - <https://linaro.atlassian.net/browse/TFC-685> TFC: Enable Geralt Chromebook TF-A test
 - In Progress
 - Patch submitted
 - Arthur: Submitted test jobs successfully
 - Chris mentioned the Google driver link for the health check firmware is not suitable for the production CI environment.
 - **I've asked Saheer to help copy the firmware to the TF download space.**
 - <https://linaro.atlassian.net/browse/TFC-653> TF-A: Google Navi Chromebook OpenCI integration
 - <https://linaro.atlassian.net/browse/TFC-848> Enable TF-A CI loop for Navi Chromebook
 - Arthur: Submitted test jobs successfully
 - Chris mentioned the Google driver link for the health check firmware is not suitable for the production CI environment.
 - **I've asked Saheer to help copy the firmware to the TF download space.**
 - <https://linaro.atlassian.net/browse/TFC-843> STM32U3 OpenCi integration
 - <https://linaro.atlassian.net/browse/TFC-844> : STM32U3 LAVA integration
 - **Arthur: Started to manually test the health check firmware**
- **Afterparty Items**
 - **Tux limit**
 - **84850 / 150000 @ 13:30 Today**

- **Ben C proposed using Shrinkwrap for private TF FVP images**
 - Hes created a ticket here <https://linaro.atlassian.net/browse/TFC-840>
 - On hold
 - Bharath to respond in due course.
- Shaun OOO 26th August - 3rd Sept
- Matt out 18th / 25th / 1st September
- **Next week's meeting is cancelled**