TF OpenCI Triage Meeting Minutes

(Sent to: tf-openci-triage@lists.trustedfirmware.org)

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Minutes - Kanban board:https://linaro.atlassian.net/jira/software/c/projects/TFC/boards/70

- **TF- A** (Olivier/Joanna)
  - https://linaro.atlassian.net/browse/TFC-824 tf-a-lts2.14-main failed
    - High
    - Done
    - Arthur - Patch submitted and issue resolved

- **TF-M** (Anton/Matt)
  - https://linaro.atlassian.net/browse/TFC-784 Allow scripts to run alongside tests running for TF-M on LAVA
    - High
    - In Progress
    - Similar priority to ERPC
    - Matt added detail working with Arthur to resolve
    - Arthur- Blocked requires Lava change.. FVP needed from Arm -Matt

  - https://linaro.atlassian.net/browse/TFC-825 NXP FRDMMCXN947 TF-M test failed
    - High
    - Resolved
    - Arthur - Done, provided the log to NXP, and Waqar submitted a patch to fix it
- **Hafnium**

- **MbedTLS**

- **OpenCI/Infrastructure**

  - https://linaro.atlassian.net/browse/TFC-799 migration of tux FVP to Arm64
    - High
    - Ben waiting for FVP images
    - Chris - FVP images need passing - Starting mid feb

  - https://linaro.atlassian.net/browse/TFC-819 LAVA: Convert health-checks binaries to a sane location

- Ben - Tuxpub artifacts are now up. Need to update LAVA configs

- **Board Changes**
  - **Arm**
    - **RPI instability - https://tf.validation.linaro.org/scheduler/device_type/bcm2837-rpi-3-b-32 - Linaro to investigate**
  - **ST**
    - https://linaro.atlassian.net/browse/TFC-786 Gradual phase out of STM32L5 and replacement with STM32WBA6
      - Issues with FW
    - https://linaro.atlassian.net/browse/TFC-787 TM32WBA6 LAVA integration
      - Arthur - Device-type was done, working on the device dict and the flasher script
  - **Google**
    - https://linaro.atlassian.net/browse/TFC-653 Google Platform 2025
      - New Chrome books have arrived