TF OpenCI Triage Meeting Minutes

(Sent to: tf-openci-triage@lists.trustedfirmware.org)

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Minutes - Kanban board:https://linaro.atlassian.net/jira/software/c/projects/TFC/boards/70

- **TF- A** (Olivier/Joanna)
  - TF-A LTS support from Bipbin on Discord
    - tf-a-lts2.8-main build failed
    - tf-a-lts-triage report
    - Arthur working with Bipin to resolve

- **TF-M** (Anton/Matt)
  - https://linaro.atlassian.net/browse/TFC-784 Allow scripts to run alongside tests running for TF-M on LAVA
    - High
    - **Blocked**
    - Similar priority to ERPC
    - Matt- Issues with disabling visualisation..Terminal issue to investigate

  - https://linaro.atlassian.net/browse/TFC-788 Enable STM32WBA6 TF-M CI loopworking on the tf-a-ci-scripts to enable CI loop for STM32WBA6 board
    - High
    - In Progress
    - Arthur working on the tf-a-ci-scripts to enable CI loop for STM32WBA6 board
- **Hafnium**
- **MbedTLS**

- **OpenCI/Infrastructure**

  - https://linaro.atlassian.net/browse/TFC-799 migration of tux FVP to Arm64
    - High
    - **Blocked**
    - Ben waiting for FVP images
    - Chris - FVP images need passing - Starting mid feb

  - https://linaro.atlassian.net/browse/TFC-819 LAVA: Convert health-checks binaries to a sane location
    - High

- ■ Resolved
- ■ Ben - Tuxpub artifacts are now up. LAVA configs updated.

- ● **Board Changes**
  - ○ **Arm**
    - ■ RPI instability - https://tf.validation.linaro.org/scheduler/device_type/bcm2837-rpi-3-b-32
      - ● Caused by https://linaro.atlassian.net/browse/TFC-826 TF: Move Artefacts from jobs to downloads.tf.o
      - ● **Resolved by Ben**
  - ○ **ST**
    - ■ https://linaro.atlassian.net/browse/TFC-786 Gradual phase out of STM32L5 and replacement with STM32WBA6
      - ● Waiting for lab deployment
    - ■ https://linaro.atlassian.net/browse/TFC-787 TM32WBA6 LAVA integration
      - ● Arthur - waiting for Lab to deploy
  - ○ **Google**
    - ■ https://linaro.atlassian.net/browse/TFC-653 Google Platform 2025
      - ● New Chrome books have arrived