TF OpenCI Triage Meeting Minutes

(Sent to: tf-openci-triage@lists.trustedfirmware.org)

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Kanban board:https://linaro.atlassian.net/jira/software/c/projects/TFC/boards/70

Mailing list: tf-openci-triage@lists.trustedfirmware.org - Join here

- **TF-A** (Olivier/Joanna)
    - Arthur Mentioned upcoming TF-A LTS work post meeting with maintainers.
    - https://linaro.atlassian.net/browse/TFC-834 Arm-On-Arm (Epic)
        - Medium
        - **In Progress**
        - https://linaro.atlassian.net/browse/TFC-799 migration of tux FVP to Arm64
            - High
            - **In Progress**
            - Ben waiting for FVP images
            - Wrote a PR for FVP docker repo which uses buildx - Chris reviewing and merging.. Needs a test and start uploading some arm64 images
            - Noticed a few failed fvp dockers which have now been patched. We are now ready to do a Arm64 builds. Ben to make CI changes with Chris. **Chris to do upload to fvp - Daniel working on the jenkins job- getting migrated to bake.. Still need to update the fvp update script.. Chris might take a while**
    - https://linaro.atlassian.net/browse/TFC-850 TF-A LTS automation
        - https://linaro.atlassian.net/browse/TFC-866 Automate LTS Patch Stack Creation from Triage Reports
            - Medium
            - Done
            - **Patch merged**
    - https://linaro.atlassian.net/browse/TFC-871 : Triage report lts-v2.14 missing potential patches.
        - High
        - Done
        - **Patch merged**

- **TF-M** (Anton/Matt)
    - https://linaro.atlassian.net/browse/TFC-784 Allow scripts to run alongside tests running for TF-M on LAVA

        - High
        - **Blocked**
        - Arthur: Blocked: Task on hold due to broken DCSU builds in TF-M
        - On hold nobody to fix - **Matt** to check - Long Term blocked - No changes not a huge priority RSE internal branch is ok public one broken.

- **Hafnium**
    - N/A

- **MbedTLS**
    - N/A

- **OpenCI/Infrastructure**
    - https://linaro.atlassian.net/browse/TFC-13 IAR Compiler Support for TF-M and MbedTLS
        - **On Hold**
        - Passed the license details and handed over the TF-M tasks, TFC-618, TFC-619, to Saheer and Matt. Matt taking a look. Matt to try locally Arm ticket status updated- Matt IAR credential in the CI now… just need to push changes to get it tested.. Then the same needs doing for MbedTLS (Bence to share the same license once TF-M done)
            - Some build issues stopping it running on TF-M
        - **Matt most of its working now just finalising platforms- fully merged**
    - **GIT being bombarded by bots**
        - **Arm infra have shut its public face down**
        - **Ben proposed he can help as he has resolved a bunch of similar issues among other projects.**
            - **Chris to make contact with relevant people.**
            - **Status could be included in the dashboard mk2**
                - **Along with Tux aws etc**
                - **Still down into the second week**

    - N1SPD 2 dropped off in the office
        - 2 running now.. Migrating to 2nd one to LAA
    - Shifting all 3 STM boards over to LAA
    - Will start RPI next
    - **FYI Linaro Lab are doing a power test on Monday 9th November 10:30am**
- Dashboard,

- n/a

- **Board Changes**

  - https://linaro.atlassian.net/browse/TFC-843 STM32U3 OpenCi integration
    - https://linaro.atlassian.net/browse/TFC-845: STM32U3 TF-M CI loop enablement
      - tf-m-ci-scripts patch submitted
      - **Done Patch merged**
    - https://linaro.atlassian.net/browse/TFC-872: migrate STM32U3 to LAA
      - bakfleet patch submitted

  - https://linaro-servicedesk.atlassian.net/servicedesk/customer/portal/40/LL-2015 : Update Chromebook LAVA configuration
    - I created this lab ticket to update the servod Docker container image, which includes a fix for a bug that may cause other Chromebooks on the same host to disconnect during firmware flashing
    - The update also includes a retry mechanism in the firmware flashing and DUT control scripts to improve reliability
    - **Ben** still taking a look

- **Afterparty Items**
  - **Tux limit**
    - **7600 / 150000 @ 13:30 Today**
  - Ben C proposed using Shrinkwrap for private TF FVP images
    - Hes created a ticket here https://linaro.atlassian.net/browse/TFC-840
      - On hold
      - Bharath to respond in due course.
  - Google Julius is asking a CI-related question: how often does a patch get rejected by CI because of a boot issue on one of the Chromebooks?
    - Ben we think we need a platform based dashboard as this is a difficult question to answer
      - Beta released https://hub.linaro.com/trusted-firmware
        - Looking for feedback