

TF OpenCI Triage Meeting Minutes

(Sent to: tf-openci-triage@lists.trustedfirmware.org)

Apr 07, 2026

Kanban board: <https://linaro.atlassian.net/jira/software/c/projects/TFC/boards/70>

Mailing list: tf-openci-triage@lists.trustedfirmware.org - Join [here](#)

- **TF- A** (Olivier/Joanna)
 - <https://linaro.atlassian.net/browse/TFC-834> Arm-On-Arm (Epic)
 - Medium
 - **On Hold**
 - Arthur: Synced with Chris, and created a ticket. This epic will be put on hold, until I complete Hafnium LTS automation, and two Chromebook OpenCI integration
- **TF-M** (Anton/Matt)
 - <https://linaro.atlassian.net/browse/TFC-784> Allow scripts to run alongside tests running for TF-M on LAVA
 - High
 - **Blocked**
 - Arthur: Blocked: Task on hold due to [broken DCSU builds](#) in TF-M
 - On hold nobody to fix
 - <https://linaro.atlassian.net/browse/TFC-788> Enable STM32WBA6 TF-M CI loop
 - High
 - Done
 - Arthur: available on tf-m nightly
- **Hafnium**
 - <https://linaro.atlassian.net/browse/TFC-779> Hafnium LTS: Automate generation of change log for minor releases based
 - Medium
 - In Progress
 - Arthur: Patch submitted awaiting review from **Bipin/Madhukar**
- **MbedTLS**
 - n/a
- **OpenCI/Infrastructure**
 - <https://linaro.atlassian.net/browse/TFC-799> migration of tux FVP to Arm64
 - High
 - **Blocked**
 - Ben waiting for FVP images
 - Chris - FVP images need passing - mid mar

- <https://linaro.atlassian.net/browse/TFC-13> IAR Compiler Support for TF-M and MbedTLS
 - **On Hold**
 - Passed the license details and handed over the TF-M tasks, [TFC-618](https://linaro.atlassian.net/browse/TFC-618), [TFC-619](https://linaro.atlassian.net/browse/TFC-619), to Saheer and Matt. Matt taking a look
- <https://linaro.atlassian.net/browse/TFC-842> S3 buckets that Ben would like to close published a list here
 - High
 - **In Progress**
 - AWS bill is still around \$500. Ben still needs approval from **Chris, Matt, Saheer** to close up S3 buckets
 - Looks like we can shut them down **Ben**
- **Board Changes**
 - **ST**
 - <https://linaro.atlassian.net/browse/TFC-786> Gradual phase out of STM32L5 and replacement with STM32WBA6
 - Done
 - Arthur: available on tf-m nightly
 - **Google**
 - <https://linaro.atlassian.net/browse/TFC-409> TF-A: Add Geralt to Google Platform in 2024
 - In Progress
 - Arthur: Servo v4 box is working. I'll pack four Servo v4 boxes, along with Geralt-01 and Geralt-02, and send them to the Cambridge lab to deploy on the TF LAVA
 - <https://linaro.atlassian.net/browse/TFC-653> Google Platform 2025
 - working on the tf-a-ci-scripts
- **Afterparty Items**
 - **Tux limit**
 - **23276 / 140000 @ 13:00 Today**
 - **Ben proposed using Shrinkwrap for private TF FVP images**
 - Hes created a ticket here <https://linaro.atlassian.net/browse/TFC-840>
 - On hold
 - Bharath to respond in due course
 - Ben we should look at the remaining Misra work with Saheer in the next couple

of weeks

- 2 Duts not working - Two of the devices have no available runners (stm32mp15x-eval, sc7180-trogdor-lazor-limozeen) and a growing queue.

- Chris on Sabbatical for 1 month starting 4th May