

TF OpenCI Triage Meeting Minutes

(Sent to: tf-openci-triage@lists.trustedfirmware.org)

June 30, 2026

Kanban board: <https://linaro.atlassian.net/jira/software/c/projects/TFC/boards/70>

Mailing list: tf-openci-triage@lists.trustedfirmware.org - Join [here](#)

- **TF- A** (Olivier/Joanna)
 - Arthur Mentioned upcoming TF-A LTS work post meeting with maintainers.
 - <https://linaro.atlassian.net/browse/TFC-834> Arm-On-Arm (Epic)
 - Medium
 - **On Hold**
 - Arthur: Synced with Chris, and created a ticket. This epic will be put on hold, until I complete ~~Hafnium LTS automation~~, and two Chromebook OpenCI integration
 - <https://linaro.atlassian.net/browse/TFC-685> TFC: Enable Geralt Chromebook TF-A test
 - High
 - Blocked
 - Arthur Patch submitted marked as blocked, waiting for lab to set up Geralt Chromebooks
 - <https://linaro.atlassian.net/browse/TFC-850> TF-A LTS automation
 - High
 - In progress
 - **Arthur: The requirements have been clarified, and I've started working on the script Target August**
 - <https://linaro.atlassian.net/browse/TFC-853> Generate LTS triage report based on supported CPUs list
 - High
 - In Progress
 - **Arthur: Upstream Review**

- **TF-M** (Anton/Matt)
 - <https://linaro.atlassian.net/browse/TFC-784> Allow scripts to run alongside tests running for TF-M on LAVA
 - High
 - **Blocked**
 - Arthur: Blocked: Task on hold due to [broken DCSU builds](#) in TF-M
 - On hold nobody to fix - Matt to check - Long Term blocked - No changes not a huge priority RSE internal branch is ok public one broken.
 - <https://linaro.atlassian.net/browse/TFC-852> TF-M Mirroring to github does not

work properly

- Medium
 - Open
 - **Anton some release branches missing will review with Nicola to review. CHris to follow up.**
- **Hafnium**
 - n/a
- **MbedTLS**
 - n/a
- **OpenCl/Infrastructure**
 - <https://linaro.atlassian.net/browse/TFC-799> migration of tux FVP to Arm64
 - High
 - **Blocked**
 - Ben waiting for FVP images
 - Chris - FVP images need passing - on hold
 - <https://linaro.atlassian.net/browse/TFC-13> IAR Compiler Support for TF-M and MbedTLS
 - **On Hold**
 - Passed the license details and handed over the TF-M tasks, [TFC-618](https://linaro.atlassian.net/browse/TFC-618), [TFC-619](https://linaro.atlassian.net/browse/TFC-619), to Saheer and Matt. Matt taking a look. Matt to try locally Arm ticket status updated- Some work on Matt to check licensing.
 - <https://linaro.atlassian.net/browse/TFC-849> QEMU: Move to Tux
 - Medium
 - **Done**
 - **Ben - PR merged**
 - **Results being reviewed**
 - Ben Started Moving Junos to LAA
 - R2 <https://tf.validation.linaro.org/scheduler/worker/laa-00093>
 - R1 and R0 next
 - **Then probably MUSCA B**
- Dashboard,
 - n/a
- **Board Changes**
 - **Google**
 - <https://linaro.atlassian.net/browse/TFC-409> TF-A: Add Geralt to Google Platform in 2024
 - In Progress
 - Arthur: Package is arrived
 - <https://linaro.atlassian.net/browse/TFC-653> Google Platform 2025
 - working on the tf-a-ci-scripts
 - <https://linaro.atlassian.net/browse/TFC-684> TFC: Enable Geralt

Chromebook on TF LAVA

- Flashed the stock firmware that Julius provided..
 - After flashing, Geralt-05 became unbootable and exhibited the same behavior as Geralt-01 in the lab..
 - **Collect logs for further investigation and reported to Julius. Blocked on google.**
 - <https://linaro.atlassian.net/browse/TFC-847> Navi Chromebook LAVA integration
 - **All devices were unlocked**
 - **Sent 4 units and 4 SuzyQ cables to Cambridge lab**
 - **Working on LAVA device-type**
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- **Afterparty Items**
 - **Tux limit**
 - **123019 / 140000 @ 13:00 Today**
 - **Upping to 150k**
 - **Ben proposed using Shrinkwrap for private TF FVP images**
 - Hes created a ticket here <https://linaro.atlassian.net/browse/TFC-840>
 - On hold
 - Bharath to respond in due course.